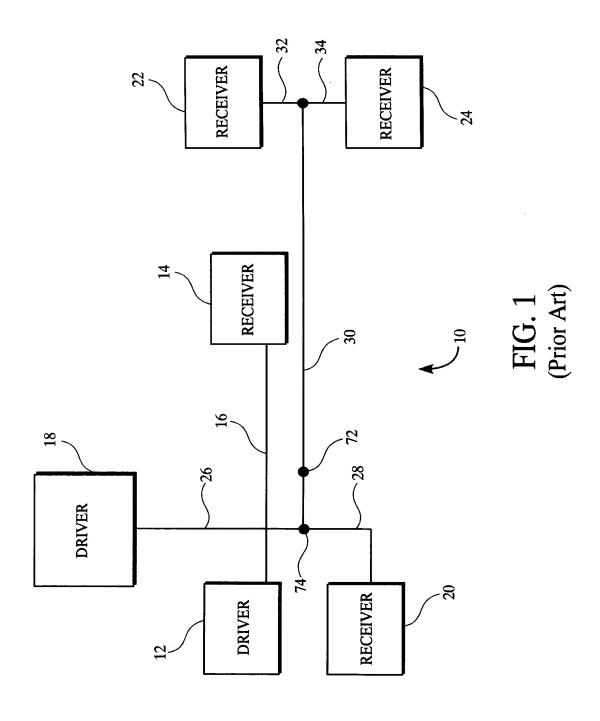
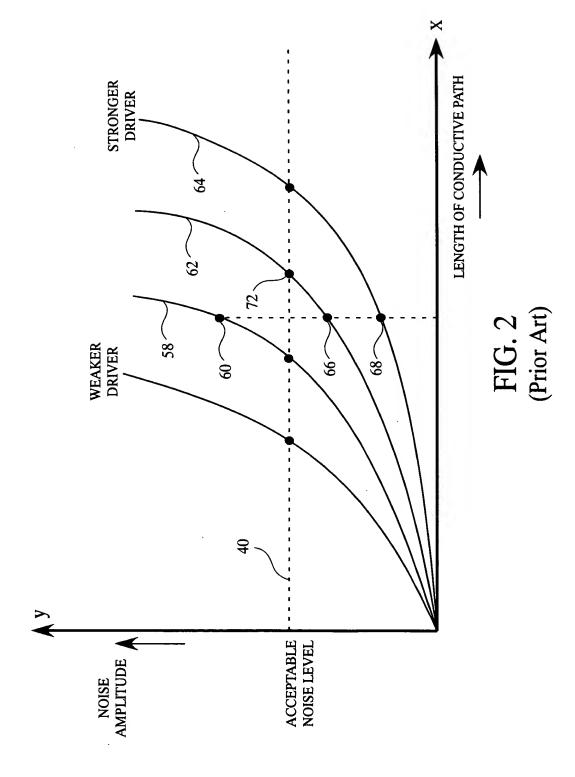
Title: METHOD FOR REDUCING NOISE IN INTEGRATED CIRCUIT LAYOUTS

Inventors: Massimo Sutera et al. Application No: 09/430,350 Atty Docket: SUNMP099









Title: METHOD FOR REDUCING NOISE IN INTEGRATED CIRCUIT LAYOUTS
Inventors: Massimo Sutera et al. Application No: 09/430,350 Atty Docket: SUNMP099



